## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): M. B. ANAND et al.		
Serial No.:		
Filed:	Group Art Unit:  Examiner:	
Title: Computer Aided Design Method and Apparatus for Modeling and Analyzing On-chip Interconnect Structures	Examiner.	
Attorney Docket No.: YOR920030315US1		
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450		
INFORMATION DISCI	LOSURE STATEMENT	
Dear Sir:		
This Information Disclosure Statement is	submitted:	
X under 37 CFR 1.97(b), or (Within three months of filing nation application; or before mailing date occurs last)	onal application; or date of entry of international of first office action on the merits; whichever	
under 37 CFR 1.97(c) together with  Statement under 37 CFR 1.97(c)  a \$180.00 fee under 37 CFR 1.  (After the CFR 1.97(b) time per allowance, whichever occurs firm	e), or 17(p), or riod, but before final action or notice of	
under 37 CFR 1.97(d) together with  Statement under 37 CFR 1.97(d)  a \$180.00 fee set forth in 37 CI  (Filed after final action or notice before payment of the issue fee	e), and FR 1.17(p). se of allowance, whichever occurs first, but	

X Applicant(s) submit herewith Form PTO 1449-Information Disclosure Citation together with copies of publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

The relevance of the attached references is that this is the closest art of which Applicant is aware. Applicant submits that the above references taken alone or in combination neither anticipate nor render obvious the present invention. Consideration of the foregoing in relation to this application is respectfully requested.

It is requested that the information disclosed herein be made of record in this application.

I hereby certify that this is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated below and is addressed to:

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Typed Name: Charles W. Peterson, Jr.

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Date of Deposit: October 21, 2003

Respectfully submitted,

Charles W. Peterson, Jr.

Attorney/Agent for Applicant(s)

Reg. No. 34406

Date: October 21, 2003

Telephone No.: (703)481-0532

PTO/SB/08A (06-03)

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Substitute for form 1449A/PTO		Complete if Known		
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INFORMATION DISCLOSURE			Filing Date	
		First Named Inventor	Anand et al.	
SI	STATEMENT BY APPLICANT		Art Unit	
		,	Examiner Name	Unknown
Sheet	1	of 2	Attorney Docket Number	YOR920030315US1

U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Document Number  Number - Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US- 5,610,833	3/11/1997	Chang et al.			
		US- 6,342,823	1/29/2002	Dansky et al.			
		US- 6,418,401	7/9/2002	Dansky et al.			
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FOREIGN PATENT DOCUMENTS							
Examiner	miner Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,		
Initials*	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	T⁵	
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Translation is attached.
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Substitute for form 1449B/PTO				Complete if Known		
				Application Number		
INFORMATION DISCLOSURE			IIRF	Filing Date		
	STATEMENT BY APPLICANT		First Named Inventor	Anand et al.		
SIAI			Art Unit			
	(use as many sheets a	s necessary)		Examiner Name	Unknown	
Sheet	2	of	2	Attorney Docket Number	YOR920030315US1	

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>		
		ALINA DEUTSCH ET AL., On-Chip Wiring Design Challenges for Gigahertz Operation, 4/1/2001			
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